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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/614,860	07/09/2003	Yasuo Inoue	29284/597	8147

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EXAMINER

CHEN, ALAN S

ART UNIT PAPER NUMBER

2182

DATE MAILED: 09/27/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/614,860

Applicant(s)

INOUE, YASUO

Examiner

Alan S Chen

Art Unit

2182

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-14 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-14 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 09 July 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. ____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892) *
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date ____.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: ____.

DETAILED ACTION

1. The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. See *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970); and, *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent is shown to be commonly owned with this application. See 37 CFR 1.130(b).

Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

Claims 1-14 rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1-14 of application #s: 10/614859, 10/614861, 10/614862, 10/614863, 10/614864. Although the conflicting claims are not identical, they are not patentably distinct from each other because applicant recites limitations that are based on the general premise of the instant application that each functional unit requires a minimum number of paths required to connect to associated functional units, e.g., the number of paths is equivalent to the number of associated functional units needed to be connected to.

Information Disclosure Statement

2. The information disclosure statement filed 07/09/2003 fails to comply with 37 CFR 1.98(a)(2), which requires a legible copy of each U.S. and foreign patent; each publication or that portion which caused it to be listed; and all other information or that portion which caused it to be listed. It has been placed in the application file, but the information referred to therein has not been considered.

U.S. Patent Documents have been considered, but Foreign Patent Documents and Other Documents have not.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

4. Claims 1-14 rejected under 35 U.S.C. 102(e) as being anticipated by No.

5,247,638 to O'Brien et al. (hereafter O'Brien).

5. As per claim 1, O'Brien discloses a storage system comprising: a plurality of channel units (Fig. 1, elements 110-0 and 110-1 and Fig. 2, elements 201-0 and 201-7) that transfers data sent from an upper-level system (Fig. 1, element 11 and 12) and transfers data to said upper-level system (see abstract), a cache unit (Fig. 1, element 113) which is connected to said plurality of channel units (Fig. 2, element 113 is connected to element 110) and in which data sent from said plurality of channel units is stored (Column 7, lines 46-65); a control unit (Fig. 1, element 111) that is connected to said cache unit (Fig. 1, element 113), and transfers or receives data to or from said cache unit (Fig. 2); a disk device in which data sent from said control unit is stored (Fig. 1, element 102-1), and a plurality of paths (Fig. 2, paths between element 202-0 and element 113 and element 202-1 and element 113), one of said paths connecting each channel unit to said cache unit (Fig. 2), wherein a number of said paths linking said plurality of channel units and said cache unit equals a number of said plurality of channel units (Fig. 2, two

paths, one for each channel unit, e.g., 202-0 has one dedicated path, while 202-1 has another dedicated path).

6. As per claim 2, O'Brien discloses claim 1, wherein said plurality of paths includes a first path (Fig. 2, line between element 202-0 and 113) that links a first channel unit (element 202-0) included in said plurality of channel units (element 202-0 and 202-1) to said cache unit (element 113), and a second path (line between element 202-1 and 113) that links a second channel unit (element 202-1) included in said plurality of channel units to said cache unit (element 113).

7. As per claim 3, O'Brien discloses claim 2, wherein said first path and said second path are independent of each other (Fig. 2, paths are not connected/dependent, each path solely dedicated for each controller and corresponding cache unit, e.g., between elements 202-0 and 113, no branching off to connect with path between 202-1 and 113).

8. As per claim 4, O'Brien discloses claim 2, wherein said first path is dedicated to communication between said first channel unit and said cache unit (Fig. 2, paths are not connected/dependent, each path solely dedicated for each controller and corresponding cache unit, e.g., between elements 202-0 and 113, no branching off to connect with path between 202-1 and 113).

9. As per claim 5, O'Brien discloses claim 4, wherein said second path is dedicated to communication between said second channel unit and said cache unit (Fig. 2, paths are not connected/dependent, each path solely dedicated for each controller and corresponding cache unit, e.g., between elements 202-0 and 113, no branching off to connect with path between 202-1 and 113).

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10. As per claim 6, O'Brien discloses claim 1, wherein among said plurality of paths, a path linking said cache unit (Fig. 1, element 113) and a predetermined channel unit included in said plurality of channel units is not the same as a path linking said cache unit and another channel unit included in said plurality of control units (Fig. 2, paths are not connected/dependent, each path solely dedicated for each controller and corresponding cache unit, e.g., between elements 202-0 and 113, no branching off to connect with path between 202-1 and 113).

11. As per claims 7 and 9, O'Brien discloses claim 2, wherein said first path directly links said first channel unit to said cache unit (Fig. 2, path between elements 202-0 and 113, note that directly linking here shows the path not branching off and shared with another path, element 203-0 is the compression circuit which is still part of the direct path of the data to the cache).

12. As per claims 8 and 10, O'Brien discloses claims 7 and 9, respectively, wherein said second path directly links said second control unit to said cache unit on a point-to-point basis, with no split/fan-out in the path (Fig. 2, path between elements 202-0 and 113, note that directly linking here shows the path not branching off and shared with another path, element 203-0 is the compression circuit which is still part of the direct path of the data to the cache).

13. As per claim 11, O'Brien discloses claim 1, wherein said disk device includes a plurality of disk drives (Fig. 1, element 122-125), and said control unit is connected to said plurality of disk drives (Fig. 1, element 121).

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14. As per claim 12, O'Brien discloses claim 1, wherein said plurality of paths are signal lines linking said cache unit and said plurality of channel units (inherently paths carry electrical signals, paths are address, data and control lines as shown in Fig. 2).

15. As per claim 13, O'Brien discloses claim 1, wherein said plurality of paths are used to communicate a read request (Fig. 2), which is issued from said upper-level system, from said plurality of channel units to said cache unit, and used to communicate data, which is read from said cache unit to said plurality of channel units (Column 9, lines 33-39, cache stores data that is accessed by both the disk drives and host).

16. As per claim 14, O'Brien discloses claim 1, wherein said plurality of paths are used to communicate a writing request (Fig. 2), which is issued from said upper-level system, and used to communicate data written from said plurality of channel units to said cache unit (Column 9, lines 33-39, cache stores data that is accessed by both the disk drives and host).

Conclusion

17. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

The following patents are cited to further show the state of the art with respect to data storage device that interface a host via cache, controllers:

U.S. Pat. No. 5,131,087 to Warr

U.S. Pat. No. 5,253,351 to Yamamoto et al.

U.S. Pat. No. 5,263,145 to Brady et al.

U.S. Pat. No. 4,633,387 to Hartung et al.

U.S. Pat. No. 4,603,380 to Easton et al.

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18. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Alan S Chen whose telephone number is 571-272-4143.

The examiner can normally be reached on M-F 8:30am - 5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jeffrey A Gaffin can be reached on (571) 272-4146. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

ASC
09/14/2004


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